

In the Claims:

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1. (Previously canceled)
  2. (Previously amended) The system of Claim 22 further comprising:  
a memory having a first port coupled to the output video data port of the data storage controller and a second port coupled to the address port of the data storage controller.
  3. (Original) The system of Claim 2, wherein the memory is a frame buffer memory.
  4. (Previously amended) The system of Claim 2 further comprising:  
a system bus interface having a first port coupled to a third port of the memory, and a second port coupled to a fourth port of the memory.
  5. (Previously amended) The system of Claim 4, wherein the third port of the memory is substantially the same as the second port of the memory.
  6. (Previously amended) The system of Claim 22, wherein the digital video transport stream is a digital video broadcast transport stream.
  7. (Previously amended) The system of Claim 6, wherein the control signals of the digital video transport stream include a clock signal, a synchronization signal, and a data valid signal.
  8. (Original) The system of Claim 7, wherein the set of control signals of the transport stream interface control includes a start of field signal to indicate the start of a frame of video.
  9. (Original) The system of Claim 8, wherein the set of control signals of the transport stream interface control signal includes a valid data output to indicate when data on the second output port of the transport stream interface control is active video data.

10. (Previously amended) The system of Claim 9, wherein the set of control signals of the data storage controller includes a valid vertical blanking interval signal to indicate when data on the output video data port of the data storage controller is present during a vertical blanking interval.

11. (Original) The system of Claim 6, further comprising:

a first video port to receive digital video of a first type, wherein the first type is not digital video broadcast stream video;

a first video interface control having an input coupled to the first video port, and having a first output port to provide the set of control signals, and a second output port to provide video graphics data; and

a select node coupled to the transport stream interface control and to the first video interface control.

12. (Original) The system of Claim 11, wherein the first video port is a zoom video port.

13. (Original) The system of Claim 11, wherein the first video port is a digital video stream port.

14. (Previously amended) A method of receiving video graphics data, the method comprising the steps of:

receiving a transport stream associated with a digital video broadcast signal, the transport stream having data signals and control signals;

generating a secondary set of control signals from the transport stream's control signals;

storing at least a portion of the transport stream data signals in a memory buffer controlled by the secondary set of control signals; and

sending the contents of the memory buffer to a system bus.

15. (Previously amended) The method of Claim 14, further comprising:  
wherein the steps of receiving, generating and storing occur when in a first mode of operation;

during a second mode of operation, performing the steps of:

receiving a digital video signal having data signals and control signals, wherein the digital video signal is of a different type than the transport stream;

generating the secondary set of control signals from the digital video signal's control signals; and

storing at least a portion of the digital video signal in the memory buffer based on the secondary set of control signals.

16. (Previously amended) The method of Claim 15, wherein the video signal is a zoom video signal.

17. (Previously amended) The method of Claim 14, wherein the memory buffer is a frame buffer.

18. A system for receiving a digital video broadcast signal, the system comprising:  
a tuner to receive a digital video broadcast signal and to provide an analog output signal;

a demodulator coupled to receive the analog output signal from the tuner, and to provide a transport stream;

a video graphics adapter coupled to receive the transport stream and having a system interface port, the video graphics adapter further includes a video engine and a video output port.

19. (Original) The system of Claim 18, wherein the video graphics adapter includes:

a memory to store at least a portion of the transport stream.

20. (Original) The system of Claim 18, further comprising:

a central processor unit coupled to the system interface port of the video graphics adapter; and

a transport demultiplexor coupled to demodulator.

21. (Previously amended) A method of storing video data, the method comprising the steps of:

in a first mode of operation, storing pixel information in a frame buffer of a video adapter, wherein one line of the frame buffer memory is representative of one line of a video image to be displayed;

in a second mode of operation, storing compressed transport stream data in the frame buffer, wherein one line of the frame buffer memory is representative of one transport stream packet.

22. (Currently amended) A video graphics system comprising:

a transport stream port to receive a digital video transport stream, the digital video transport stream including a data stream and control signals;

a transport stream interface control having an input coupled to the transport stream port, and having a first output port to provide a set of control signals, and a second output port to provide [a] video graphics data; and

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[the] a data storage controller having a first input port coupled to the first output port of the transport stream interface control to accept the set of control signals of the transport stream interface control, a second input port coupled to a second output port of a transport stream interface control to accept video graphics data, an output address port to provide an address value, an output video data port to provide video data, and at least one pair of a plurality of internal control ports to communicate control signals within the data storage controller.

23. (Previously added) The system of Claim 7, wherein the control signals of the digital video transport stream further include an error signal, indicating there is an error in the transport stream.